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Sie sind Lehrerinnen am Goethe-Institut Niederlande und verfügen über langjährige Erfahrungen in Deutschkursen für fremdsprachige Lerner. Die Komponenten : Es enthält ein integriertes Kurs- und Arbeitsbuch mit beigefügter Audio-CD zur Aussprache- und Hörverstehensschulung ebenfalls Lösungsheft. Es bietet auch nebenamtliche Online-Übungen im Internet. Bei Erkundungen C1 gibt es ein einstimmiges Lehrerhandbuch mit methodischen Hinweisen und zahlreichen ergänzenden Arbeitsblätter. Der Inhalt : Es besteht aus acht Kapiteln. Jeder Kapitel ist in vier Teile von A bis D gegliedert. Der Teil A bietet Lese- und Hörtexte, Wortschatztraining, Übungen zur mündlichen und schriftlichen Kommunikation und Grammatikübungen zu einem Thema. Hier behandeln und trainiern Sie wesentlich Fertigkeiten. Im Teil B befinden sich Texte und Übungen, die auf erfreuliche Weise das Thema verbreitern. Der Teil C ist Übungssteil. Er stellt systematisierende Übersichten und zahlreiche Übungen und die Ausweitung der Grammatikkenntnisse. Teil D besteht aus zwei Inhaltsstoffen: ernste Redemittel und Selbstevaluation. Erkundungen C1 PDF +c2 Erkundungen B2 PDF +100 Stunden Deutschland PDF +Lösungen +Mit Erfolg zu telc C1 Hochschule c1 + c2: Mega Free Download Themen aktuell 1 2 3 PDF + Free Download Testdaf Musterprüfung (Beispiel+Lösungen) Share on Facebook Share on Pinterest Share on WhatsApp Share on Telegram Academia.edu uses cookies to personalize content, tailor ads and improve the user experience. By using our site, you agree to our collection of information through the use of cookies. To learn more, view our Privacy Policy. Top Artificial intelligence (AI) requires extreme computational horsepower, but Maxim Integrated is cutting the power cord from AI insights. The MAX78000 is a new breed of AI microcontroller built to enable neural networks to execute at ultra-low power and live at the edge of the IoT. Our hardware-based convolutional neural network (CNN) accelerator enables battery-powered applications to execute AI inferences while spending only microjoules of energy. Parametric specs for Microcontrollers MCU Core ARM Cortex-M4F Internal Flash (KBytes) 512 Core Clock Speed (MHz) (max) 100 Internal SRAM (KBytes) 128 Package/Pins CTBGA-CU/81 BudgetaryPrice (See Notes) 9.91 Design & Development Click any title below to view the detail page where available. Description/Applications The MAX78000 evaluation kit (EV kit) provides a platform for leveraging the capabilities of the MAX78000 to build new generations of artificial intelligence (AI) devices. Onboard hardware includes a digital microphone, a gyroscope/accelerometer, parallel camera module support and a 3.5in touch-enabled color TFT display. A secondary display is driven by a power accumulator for tracking device power consumption over time. Uncommitted GPIO as well as analog inputs are readily accessible through 0.1in pin headers. Primary system power as well as UART access is provided by a USB Micro-B connector. A USB to SPI bridge provides rapid access to onboard memory, allow- ing large networks or images to load quickly. View Details Features Power Accumulator with Dedicated Display to Track Device Power over Time Onboard Digital Microphone Onboard Accelerometer/Gyroscope SWD JTAG 10-Pin Header RISC-V Coprocessor JTAG 10-Pin Header 16M-Byte QSPI Flash Select GPIOs Accessible through 0.1in Headers Four ADC Inputs with Optional AA Filter Touch-Enabled, 3.5in, 320 x 240 Color TFT Display UART Access through USB Bridge QSPI Memory Access through USB Bridge All IC Power Rails May Be Isolated by Jumpers for Individual Current Measurements Two General-Purpose LEDs and Two General- Purpose Pushbutton Switches Description/Applications The MAX78000FTHR is a rapid development platform to help engineers quickly implement ultra low-power, artificial intelligence (AI) solutions using the MAX78000 Arm® Cortex® M4F processor with an integrated Convolutional Neural Network accelerator. The board also includes the MAX20303 PMIC for battery and power management. The form factor is 0.9in x 2.6in dual-row header footprint that is compatible with Adafruit Feather Wing peripheral expansion boards. The board includes a variety of peripherals, such as a CMOS VGA image sensor, digital microphone, low-power stereo audio CODEC, 1MB QSPI SRAM, micro SD card connector, RGB indicator LED, and pushbutton. The MAX78000FTHR provides a power-optimized flexible platform for quick proof-of-concepts and early software development to enhance time to market. Go to to get started developing with this board. View Details Features MAX78000 Microcontroller Dual Core: Arm Cortex-M4 Processor with FPU, 100MHz, RISC-V Coprocessor, 60MHz 512KB Flash Memory 128KB SRAM 16KB Cache Convolutional Neural Network Accelerator 12-Bit Parallel Camera Interface MAX20303 Wearable PMIC with Fuel Gauge Charge from USB On-Board DAPLink Debug and Programming Interface for Arm Cortex-M4 processor with FPU Breadboard Compatible Headers Micro USB Connector Micro SD Card Connector Integrated Peripherals RGB Indicator LED User Pushbutton CMOS VGA Image Sensor Low-Power Stereo Audio CODEC Digital Microphone SWD Debugger Virtual UART Console 10-Pin Cortex Debug Header for RISC-V Coprocessor MaximIntegratedAI (GitHub.com) Description/Applications The MAX78000 evaluation kit (EV kit) provides a platform for leveraging the capabilities of the MAX78000 to build new generations of artificial intelligence (AI) devices. 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Parametric specs for Microcontrollers MCU Core ARM Cortex-M4F Internal Flash (KBytes) 512 Core Clock Speed (MHz) (max) 100 Internal SRAM (KBytes) 128 Package/Pins CTBGA-CU/81 BudgetaryPrice (See Notes) 9.91 Dual-Core Ultra-Low-Power Microcontroller Arm Cortex-M4 Processor with FPU up to 100MHz 512KB Flash and 128KB SRAM Optimized Performance with 16KB Instruction Cache Optional Error Correction Code (ECC-SEC-DED) for SRAM 32-Bit RISC-V Coprocessor up to 60MHz Up to 52 General-Purpose I/O Pins 12-Bit Parallel Camera Interface One I2S Master/Slave for Digital Audio Interface Neural Network Accelerator Highly Optimized for Deep Convolutional Neural Networks 442k 8-Bit Weight Capacity with 1,2,4,8-Bit Weights Programmable Input Image Size up to 1024 x 1024 pixels Programmable Network Depth up to 64 Layers Programmable per Layer Network Channel Widths up to 1024 Channels 1 and 2 Dimensional Convolution Processing Streaming Mode Flexibility to Support Other Network Types, Including MLP and Recurrent Neural Networks Power Management Maximizes Operating Time for Battery Applications Integrated Single-Inductor Multiple-Output (SIMO) Switch-Mode Power Supply (SMPS) 2.0V to 3.6V SIMO Supply Voltage Range Dynamic Voltage Scaling Minimizes Active Core Power Consumption 22.2µA/MHz While Loop Execution at 3.0V from Cache (CM4 Only) Selectable SRAM Retention in Low-Power Modes with Real-Time Clock (RTC) Enabled Security and Integrity Available Secure Boot AES 128/192/256 Hardware Acceleration Engine True Random Number Generator (TRNG) Seed Generator Audio Processing: Multi-Keyword Recognition, Sound Classification, Noise CancellationFacial RecognitionObject Detection and ClassificationTime-Series Data Processing: Heart Rate/Health Signal Analysis, Multi-Sensor Analysis, Predictive Maintenance Artificial intelligence (AI) requires extreme computational horsepower, but Maxim is cutting the power cord from AI insights. The MAX78000 is a new breed of AI microcontroller built to enable neural networks to execute at ultra-low power and live at the edge of the IoT. This product combines the most energy-efficient AI processing with Maxim's proven ultra-low power microcontrollers. Our hardware-based convolutional neural network (CNN) accelerator enables battery-powered applications to execute AI inferences while spending only microjoules of energy. The MAX78000 is an advanced system-on-chip featuring an Arm® Cortex®-M4 with FPU CPU for efficient system control with an ultra-low-power deep neural network accelerator. The CNN engine has a weight storage memory of 442KB, and can support 1-, 2-, 4-, and 8-bit weights (supporting networks of up to 3.5 million weights). The CNN weight memory is SRAM-based, so AI network updates can be made on the fly. The CNN engine also has 512KB of data memory. The CNN architecture is highly flexible, allowing networks to be trained in conventional toolsets like PyTorch® and TensorFlow®, then converted for execution on the MAX78000 using tools provided by Maxim. In addition to the memory in the CNN engine, the MAX78000 has large on-chip system memory for the microcontroller core, with 512KB flash and up to 128KB SRAM. Multiple high-speed and low-power communications interfaces are supported, including I2S and a parallel camera interface (PCIF). The device is available in a 81-pin CTBGA (8mm x 8mm, 0.8mm pitch) package. Search our knowledge base for answers to your technical questions. Filtered Search Our dedicated team of Applications Engineers are also available to answer your technical questions. Visit our support portal . Sampling: Selecting the Sample button above will redirect to the third-party ADI Sample Site. The part selected will carry over to your cart on this site once logged in. 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